



PTO/SB/08a/b (08-03)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	10/612,974
				Filing Date	July 7, 2003
				First Named Inventor	Chandra Mouli
				Art Unit	N/A
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	M4065.0941/P941

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴ -Kind Code ⁵ (if known)				

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CA	ILIANA L. FUJIMORI et al., "A 256x256 CMOS DIFFERENTIAL PASSIVE PIXEL IMAGER WITH FPN REDUCTION TECHNIQUES," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 35, No.12, December 2000.	
	CB	PETER SINGER, "Strained Silicon Ready for Prime Time," <i>Semiconductor International</i> , 11/1/2002.	

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3/10/06



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Substitute for form 1449A/B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	10/612,974-Conf. #4728
				Filing Date	July 7, 2003
				First Named Inventor	Chandra Mouli
				Art Unit	2871
				Examiner Name	Not Yet Assigned
				Attorney Docket Number	M4065.0941/P941
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
AA	AA	US-6,310,366	10-30-01	Rhodes et al.	

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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
	BA	EP 0 940 854 A2	09-08-99			
	BB	WO 02/33755 A2	04-25-02			

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	CA	FREEMAN, G. JAGANNATHAN, B., ZAMDMER, R., GROVES, SINGH, Y., TRETIAKOV, KUMAR, M., JOHNSON, J., PLOUCHART, J., GREENBERT, D., KOESTER, S., SCHAUB, J. Integrated SiGe and Si Device Capabilities and Trends for Multi-Gigahertz Applications, International Journal of High Speed Electronics and Systems, Vol. 13, No. 1 (2003) pgs. 175-219			
	CB	MAITI, C., BERA, L. AND CHATTOPADHYAY, S. Topical Review Strained-Si Heterostructure Field Effect Transistors, Semiconductor Science Technology 13 (1998) pgs. 1225-1246.			

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Examiner Signature		Date Considered	3/10/06
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DSMDB.1877720.